



1374.43296X00

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. ICHINOSE, et al.
Serial No.: 10/733,377
Filed: DECEMBER 12, 2003
For: FABRICATION METHOD OF SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE
Group AU: 2822
Examiner: Toniae M. Thomas
Confirm. No.: 8552

ADDITIONAL CLAIMS FEE TRANSMITTAL**Mail Stop: AMEND - FEE**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 28, 2006

Sir:

Transmitted herewith is an **Amendment** in the above-identified application.

- ☐ No additional fee is required.
☒ Also attached: **Credit Card Payment Form**

The fee (if applicable) has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	31	30	1	x \$50 =	\$50.00
Independent Claims	5	3	2	x \$200 =	\$400.00
TOTAL FEE DUE					\$450.00

Docket No.: 1374.43296X00
Serial No. 10/733,377
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- [X] A Credit Card Payment form in the amount of **\$450.00** is attached.
- [X] The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment, to Deposit Account No. 01-2135, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully Submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

By



William I. Solomon
Registration No. 28,565

WIS/ksh
1300 N. Seventeenth Street
Suite 1800
Arlington, VA 22209
Telephone: (703) 236-6620
Facsimile: (703) 312-6666